Appendix A: Intel's Objections to BIAX's Proposed Changes To Its Contentions

The state of the s	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	fanium 2
Location of			The state of the s
Change in			,
Part 1 of 3)	Nature of Change to	Basis Kor Objection	Cite to Public Document
n. 1 112.01.		The state of the s	(Wilet applicable)
Fg. 1, DIOCK	Insert a contention covering	Inis is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium compiler.	Guide, at 31-33 (2001)
Pg. 1, block	Change the contention from	This is a substantive change to BIAX's infringement	Ward Decl. Ex. M, Intel®
2, line 3-4	covering Itanium's execution	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
	units to covering Itanium's issue	structure by changing the contention from covering	Manual for Software
	ports	the Itanium execution units to covering the Itanium	Development and Optimization,
		issue ports	at 26-28 (2004)
Pg. 1, block	Change "architectural stops" to	This is a substantive change to BIAX's infringement	Ward Decl. Ex. P, Marsha Eng,
2, line 5	''stop bits'	theory. BIAX seeks to accuse a new or different	et. al, "Mesocode: Optimizations
		structure by changing the identified structure from	for Improving Fetch Bandwidth
		"architectural stops" to "stop bits".	of Itanium Processors",
			Workshop on Complexity-
			Effective Design (WCED), May 2002, at 2.
Pg. 3, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, line 27	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 3, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
2, lines 3-4	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
,		structure by adding a contention covering the	Assembly Language Reference
	a de desta de la companya que que esta de la companya que que esta de la companya que que esta de la companya que esta de la c	Itanium assembler in addition to the Itanium	Guide, at 31-33 (2001)

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent. Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
	The state of the s	compiler.	
Pg. 3, block	Change citation of "the EXP stage of the nipeline" to "I ogic	This is a substantive change in BIAX's infringement	Ward Decl. Ex. Q, Intel®
pg. 4, block 1, line 1		structure by changing the identified structure from "the EXP stage of the pipeline" to "logic in the IFRand ISD"	Developer's Manual, at 2-4 to 2-5 (2002)
Pg. 4, block	Insert "to issue ports"	This is a substantive change in BIAX's infingement	Ward Decl. Ex. M, Intel®
1, line 3		theory. BIAX seeks to accuse a new or different structure by adding a contention that the instructions	Itanium® 2 Processor Reference Manual for Software
		are delivered "to issue ports."	Development and Optimization, at 26-28 (2004)
Pg. 4, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, 11103 4-0	MOLCOL AND MOLENING CORE	above.	
Pg. 6 block 2, lines 1-4	Change the contention from covering Itanium's "M units" to	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different	Ward Decl. Ex. M, Intel®
	covering Itanium's "issue ports	structure by changing the identified structure from	Manual for Software
	and their associated execution resources".	the "M units" to the "issue ports and their associated execution resources"	Development and Optimization, at 26-28 (2004)
Pg. 6, block 2, lines 4-5	Insert citation to Itanium Merced code.	This citation supports the substantive change to BIAX's infringement theory objected to in the row	N/A
		above.	
Pg. 6, block 2, lines 7-8	Change the contention from covering Itanium's "M units" to	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference
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Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
	and their associated execution resources".	the "M units" to the "ports and their associated execution resources"	Development and Optimization, at 26-28 (2004)
Pg. 6, block 2, lines 9-10	Insert citation to Itanium McKinley code.	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 6, block 3, line 1	Adds contention that the "crossbar connects the" issue ports.	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a citation to additional structure in the Itanium processor	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)
Pg. 6, block 3, lines 1-2	Changes identification of the "Instruction Buffer" as the source of instructions to identification of the "ISD unit" as the source.	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing its identification of accused structure in the Itanium processor.	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)
Pg. 6, block 3, lines 3-4	Changes identification of the Itanium structure that processes instructions from "functional units" to "issue ports, each issue port having execution resources".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing its identification of accused structure in the Itanium processor from "functional units" to "issue ports, each issue port having execution resources".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 6, block 3, lines 4-6	Insert citation to Itanium Merced and McKinley code.	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 10, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different	Ward Decl. Ex. N, Intel® Itanium TM Architecture

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
		structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Assembly Language Reference Guide, at 31-33 (2001)
Pg. 10, block 2, lines 3-4	Change the contention from covering Itanium's execution units to covering Itanium's issue ports	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the contention from covering the Itanium execution units to covering the Itanium issue ports	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 10, block 2, line 4	Insert contention that the mapping of instruction slots to issue ports "represents logical processor numbers for the instructions"	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding an additional contention that the mapping of instruction slots to issue ports "represents logical processor numbers for the instructions", which was missing entirely from the original contention.	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 10, block 2, line 6	Insert contention that "the added intelligence also includes stop bits, which represent instruction firing times for the instructions"	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding an additional contention that "the added intelligence also includes stop bits, which represent instruction firing times for the instructions".	Ward Decl. Ex. P, Marsha Eng, et. al, "Mesocode: Optimizations for Improving Fetch Bandwidth of Itanium Processors" Workshop on Complexity- Effective Design (WCED), May 2002, at 2.
Pg. 12, block 1, lines 10-12	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
Pg. 12, block 2, line 3	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 12, block 3, lines 3-5	Change citation of "the EXP stage of the pipeline" to "Logic in the IFR (instruction fetch and rotate) unit and ISD(instruction dispersal unit)"	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "the EXP stage of the pipeline" to "logic in the IFRand ISD".	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)
Pg 12, block 3, line 7	Insert "to issue ports"	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention that the instructions are delivered "to issue ports."	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 12, block 3, lines 8-10	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 14, block 2, lines 1-2 Pg 14, block 2 lines 3.11.	Change the contention from covering Itanium's "functional units" to covering Itanium's "issue ports and their associated execution resources". Insert citation to Itanium Merced and McKinley, code	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to the "issue ports and their associated execution resources" This citation supports the substantive change to BIAX's infringement theory objected to in the row	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004) N/A
7, IIIICS J-11,	INICIOCA AIRA INICIMINES COMO	DIAA S IIIIIIII Beinein meory objected to in the row	

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in			
BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 1 of 3)	Contention	Basis For Objection	(where applicable)
Pg. 15, block		above.	
1, mos 1-3 Pa 16 block	Adds contention that the	This is a substantive change in BIAX's infringement	Ward Decl Fx O Intol®
1, lines 1-6	"crossbar connects the" issue	theory. BIAX seeks to accuse a new or different	Maid Dex. 2, met & Itanium & 2 Processor Hardware
	ports.	structure by adding a citation to additional structure	Developer's Manual, at 2-4 to 2-
Pg 16 block	Changes identification of the	This is a substantive change in BIAX's infringement	Ward Decl. Ex. O. Intel®
1, lines 1-6	"Instruction Buffer" as the	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Hardware
	source of instructions to	structure by changing its identification of accused	Developer's Manual, at 2-4 to 2-
	identification of the "ISD unit"	structure in the Itanium processor.	5 (2002)
	as the source.		
Pg. 16, block	Changes identification of the	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
1, lines 1-6	Itanium structure that processes	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
	instructions from "functional	structure by changing its identification of accused	Manual for Software
	units" to "issue ports, each issue	structure in the Itanium processor from "functional	Development and Optimization,
	port having execution	units" to "issue ports, each issue port having	at 26-28 (2004)
	resources	execution resources.	and the second s
Pg. 16, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, lines 7-8	Merced and McKinley code.	BIAX's infringement theory objected to in the two	
		rows above.	
Pg. 19, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium	<i>Guide</i> , at 31-33 (2001)
		compiler.	

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
Pg. 21, block 1, line 11-13	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg, 21, block 2, line 3	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 22, block 2, lines 1-2	Change the contention from covering Itanium's "functional units" to covering Itanium's "issue ports and their associated execution units".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to the "issue ports and their associated execution units"	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 22, block 2, lines 3-15	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 23, block 2, line 1	Adds contention that the "crossbar connects the" issue ports.	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a citation to additional structure in the Itanium processor	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)
Pg. 23, block 2, lines 1-2	Changes identification of the "Instruction Buffer" as the source of instructions to identification of the "ISD unit" as the source.	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing its identification of accused structure in the Itanium processor.	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent. Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3) Pg. 23, block 2, lines 3-4	Nature of Change to Contention Changes identification of the Itanium structure that processes instructions from "functional	Basis For Objection This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing its identification of accused	Cite to Public Document (where applicable) Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference
Pg. 23, block 2, lines 4-6	units" to "issue ports, each issue port having execution resources" Insert citation to Itanium Merced and McKinley code.	structure in the Itanium processor from "functional units" to "issue ports, each issue port having execution resources". This citation supports the substantive change to BIAX's infringement theory objected to in the three	Development and Optimization, at 26-28 (2004)
Pg. 26, block 1, lines 4-5	Change the contention from covering Itanium's "functional units" to covering Itanium's "issue ports and their associated execution resources".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to the "issue ports and their associated execution resources"	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 26, block 1, lines 8-19	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 28, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 29, block 1, line 28-30	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
Pg. 30, block 1, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 30, block 1, lines 11-13	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 30, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 30, block 2, lines 11-13	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 30, block 3, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 31, block 1, line 22-25	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 32, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to	Basis For Objection	Cite to Public Document (where applicable)
2, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 32, block 2, line 2	Change "architectural stops" to "stop bits'	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	Ward Decl. Ex. P, Marsha Eng, et. al, "Mesocode: Optimizations for Improving Fetch Bandwidth of Itanium Processors" Workshop on Complexity- Effective Design (WCED), May 2002, at 2.
Pg. 32, block 2, lines 5-6	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A
Pg. 32, block 3, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 32, block 3, line 3	Changes "execution unit type" to "issue port"	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit type" to the Itanium "issue port".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 33, block	Insert citation to Itanium	This citation supports the substantive change to	N/A

	Intel's Objections to BIA	4X's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of			
Change in	Nature of Change to		Cite to Public Document
(Part 1 of 3)	Contention	Basis For Objection	(where applicable)
1, lines 2-3	assembler code	BIAX's infringement theory objected to in the two	
Pg. 33, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
2, line l	Itanium's assembler	theory. BIAX seeks to accuse a new or different structure by adding a contention covering the	Itanium 124 Architecture Assembly Language Reference
		Itanium assembler in addition to the Itanium compiler.	Guide, at 31-33 (2001)
Pg. 33, block	Changes "execution unit type"	This is a substantive change to BIAX's infringement	Ward Decl. Ex. M, Intel®
2, line 3	to "issue port"	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
		structure by changing the accused structure from the	Manual for Software
		Itanium "execution unit type" to the Itanium "issue	Development and Optimization,
		port".	at 26-28 (2004)
Pg. 33, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 5-6	assembler code	BIAX's infringement theory objected to in the two	
		rows above.	
Pg. 33, block	Change "functional units" to	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
3, line 1	"execution units associated with	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
	the issue ports".	structure by changing the identified structure from	Manual for Software
		the "functional units" to "execution units associated	Development and Optimization,
		with the issue ports"	at 26-28 (2004)
Pg. 34, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, lines 1-13	Merced and McKinley code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 35, block	Change "11 issue ports" to "the	This is a substantive change in BIAX's infringement	Ward Decl. Ex. Q, Intel®
2, IIIe I	ISD (Instruction dispersal) unit	ulcolly. DIAA seeks to accuse a new of ullicities	THURSE TI LOCESSON TIME WHAT C

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
		structure by changing the identified structure for storing instructions from the "11 issue ports" to "the ISD (instruction dispersal) unit"	Developer's Manual, at 2-4 to 2-5 (2002)
Pg. 35, block 2, lines 3-22	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 36, block 2, line	Change "functional unit" to "issue port".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional unit" to the "issue port".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 36, block 2, lines 2-3	Insert "by the ISD (instruction dispersal) unit".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by inserting additional accused structure, the "the ISD (instruction dispersal) unit".	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)
Pg. 36, block 2, line 4	Change "processor element" to "issue port"	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "processor element" to the "issue port".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 36, block 2, lines 5-8	Insert citation to Itanium Merced and McKinley code.	This citation supports the substantive change to BIAX's infringement theory objected to in the three rows above.	N/A Word Ped Ev M Latel®
Fg. 38, block 2, line 2	Change the contention from covering Itanium's "functional	this is a substantive change in BIAX's intringement theory. BIAX seeks to accuse a new or different	ward Decl. Ex. M, mel® Itanium® 2 Processor Reference

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
	unit" to covering Itanium's "issue port and its associated execution resources".	structure by changing the identified structure from the "functional unit" to the "issue port and its associated execution resources"	Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 38, block 2, lines 3-16	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 40, block 1, lines 4-5	Change the contention from covering Itanium's "functional unit" to covering Itanium's "issue ports and their associated execution resources".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional unit" to the "issue ports and their associated execution resources"	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 40, block 1, lines 7-19	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 42, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 43, block 1, line 29-30	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 44, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
		Itanium assembler in addition to the Itanium compiler.	Guide, at 31-33 (2001)
Pg. 44, block 2, line 2	Change "architectural stops" to "stop bits'	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	Ward Decl. Ex. P, Marsha Eng, et. al, "Mesocode: Optimizations for Improving Fetch Bandwidth of Itanium Processors", Workshop on Complexity-Effective Design (WCED), May 2002, at 2.
Pg. 44, block 2, lines 5-6	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A
Pg. 45, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 45, block 2, line 3	Changes "execution unit type" to "issue port"	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit type" to the Itanium "issue port".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 45, block 2, lines 5-6	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 1 of 3)	Contention	Basis For Objection	(where applicable)
Pg. 45, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
3, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium compiler.	Guide, at 31-33 (2001)
Pg. 45, block	Changes "execution unit type"	This is a substantive change to BIAX's infringement	Ward Decl. Ex. M, Intel®
3, line 3	to "issue port"	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
		structure by changing the accused structure from the	Manual for Software
		Itanium "execution unit type" to the Itanium "issue	Development and Optimization,
		port".	at 26-28 (2004)
Pg. 45, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
3, lines 5-6	assembler code	BIAX's infringement theory objected to in the two	
		rows above.	
Pg. 46, block	Change "functional units" to	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
2, line 1	"execution resources associated	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
	with the issue ports".	structure by changing the identified structure from	Manual for Software
		the "functional units" to "execution resources	Development and Optimization,
		associated with the issue ports"	at 26-28 (2004)
Pg. 46, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 3-15	Merced and McKinley code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 47, block	Change "11 issue ports" to "the	This is a substantive change in BIAX's infringement	Ward Decl. Ex. Q, Intel®
2, line 1	ISD (instruction dispersal) unit"	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Hardware
		structure by changing the identified structure for	Developer's Manual, at 2-4 to 2-5 (2002)
		Storing instructions from the 11 issue ports to the	2 (2002)

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 1 of 3)	Contention	Basis For Objection	(where applicable)
		ISD (instruction dispersal) unit	and the state of t
Pg. 47, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 3-22	Merced and McKinley code	BIAX's infringement theory objected to in the row above.	
Pg. 48, block	Change "functional units" to	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
2, line 1	"issue ports".	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
	ı	structure by changing the identified structure from	Manual for Software
		the "functional units" to the "issue ports".	Development and Optimization, at 26-28 (2004)
Pg. 48, block	Change "processor element" to	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
2, line 4	''issue port''	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
		structure by changing the identified structure from	Manual for Software
		the "processor element" to the "issue port".	Development and Optimization, at 26-28 (2004)
Pg. 48, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 5-8	Merced and McKinley code.	BIAX's infringement theory objected to in the two rows above.	
Pg. 50, block	Change the contention from	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
2, lines 1-2	covering Itanium's "processor	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
.	element" to covering Itanium's	structure by changing the identified structure from	Manual for Software
	"issue port and its associated	the "processor element" to the "issue port and its	Development and Optimization,
	execution resources".	associated execution resources"	at 26-28 (2004)
Pg. 50, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 3-15	Merced and McKinley code	BIAX's infringement theory objected to in the row	
		above.	

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of			
BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 1 of 3)	Contention	Basis For Objection	(where applicable)
Pg. 54, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		tanium assembler in addition to the itanium compiler.	Guide, al 31-33 (2001)
Pg. 55, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, lines 10-12	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 55, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium	Guide, at 31-33 (2001)
		compiler.	
Pg. 55, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 10-12	assembler code	BIAX's infringement theory objected to in the row	
		above.	The state of the s
Pg. 55, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
3, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium	Guide, at 31-33 (2001)
		compiler.	
Pg. 55, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
3, lines 3-5	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 57, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of			
Change in RIAX EV D	Nature of Change to		Cite to Public Document
(Part 1 of 3)	Contention	Basis For Objection	(where applicable)
2, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium	Guide, at 31-33 (2001)
		compiler.	distribution and distribution in
Pg. 57, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 5-6	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 58, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium	Guide, at 31-33 (2001)
		compiler.	The state of the s
Pg. 58, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 5-6	assembler code	BIAX's infringement theory objected to in the row	
.		above.	
Pg. 58, block	Insert a contention covering	This is a substantive change to BIAX's infringement	Ward Decl. Ex. N, Intel®
3, line 1	Itanium's assembler	theory. BIAX seeks to accuse a new or different	Itanium TM Architecture
		structure by adding a contention covering the	Assembly Language Reference
		Itanium assembler in addition to the Itanium	Guide, at 31-33 (2001)
		compiler.	
Pg. 58, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
3, lines 4-5	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 58, block	Change "11 issue ports" to "the	This is a substantive change in BIAX's infringement	Ward Decl. Ex. Q, Intel®
4, line 2	ISD (instruction dispersal) unit"	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Hardware

J	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Nature of C	Nature of Change to	Basis For Ohjection	Cite to Public Document (where applicable)
		structure by changing the identified structure for storing instructions from the "11 issue ports" to "the	Developer's Manual, at 2-4 to 2-5 (2002)
nsert cit Merced	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row	N/A
Change "func".issue ports".	Change "functional units" to "issue ports".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to the "issue ports".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Insert c Mercec	Insert citation to Itanium Merced and McKinley code.	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Change "fun "issue port".	Change "functional unit" to "issue port".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional unit" to the "issue port".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Insert Merce	Insert citation to Itanium Merced and McKinley code.	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Insert	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference

	Intel's Objections to BIA	Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 1 of 3)	Contention	Basis For Objection	(where applicable)
		Itanium assembler in addition to the Itanium	Guide, at 31-33 (2001)
		compiler.	
Pg. 66, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, lines 11-13	assembler code	BIAX's infringement theory objected to in the row	
		above.	The state of the s
Pg. 67, block	Change the contention from	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
1, lines 1-2	covering Itanium's "functional	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Reference
	unit" to covering Itanium's	structure by changing the identified structure from	Manual for Software
	"issue port and its associated	the "functional unit" to the "issue port and its	Development and Optimization,
	execution resources".	associated execution resources"	at 26-28 (2004)
Pg. 67, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, lines 3-15	Merced and McKinley code	BIAX's infringement theory objected to in the row	
		above.	The state of the s
Pg. 68, block	Changes the contention from	This is three substantive changes in BIAX's	Ward Decl. Ex. Q, Intel®
2, lines 1-3	identifying a "back end (BE)"	infringement theories. BIAX seeks to accuse a new	Itanium® 2 Processor Hardware
	that connects "the function	or different structure by changing the accused	Developer's Manual, at 2-4 to 2-
	units" to the "Instruction Buffer	structure from a "back end (BE)" that connects "the	5 (2002)
	(IB)" to identifying a "crossbar"	function units" to the "Instruction Buffer (IB)", to a	
	that connects the "plurality of	"crossbar" that connects the "plurality of issue	
	issue ports" with the "ISD unit"	ports" with the "ISD unit"	THE PROPERTY AND ADDRESS OF THE PROPERTY ADDRESS OF THE PROPERTY AND ADDRESS OF THE PROPERTY ADDRESS OF THE PROPERTY AND ADDRESS OF THE PROPERTY ADDRESS OF THE PROPERTY AND ADDRESS OF THE PROPERTY ADDRESS OF THE PROPERTY AND ADDRESS OF THE PROPERTY ADDRE
Pg. 68, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 3-5	Merced and McKinley code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 69, block	Change "functional units" to	This is a substantive change in BIAX's infringement	Ward Decl. Ex. M, Intel®
2, line 1	"execution resources associated	theory. BIAX seeks to accuse a new or different	Itanium® 2 Processor Kejerence

	Intel's Objections to BIA	X's Proposed Changes for '755 Patent, Itanium and Itanium 2	tanium 2
Location of Change in BIAX Ex. D (Part 1 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
	with the issue ports".	structure by changing the identified structure from the "functional units" to "execution resources associated with the issue ports"	Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 70, block 2, lines 1-2	Change citation of "The Instruction Buffer (IB)" to "Logic in the IFR (instruction fetch and rotate) unit and ISD(instruction dispersal unit)"	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "The Instruction Buffer (IB)" to "logic in the IFRand ISD".	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)
Pg. 70, block 2, line 2	Change "functional units" to "issue ports".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to "issue ports"	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 70, block 2, lines 5-7	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A
Pg. 71, block 2, line 1	Change the contention from covering Itanium's "functional units" to covering Itanium's "issue ports and their associated execution resources".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to the "issue ports and their associated execution resources"	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 71, block 2, lines 2-3	Change the contention from covering Itanium's "functional elements" to covering Itanium's "issue ports and their associated	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional elements" to the "issue ports and	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization,

Itanium 2		Cite to Public Document	(where applicable)	at 26-28 (2004)	Ward Decl. Ex. M, Intel®	Itanium® 2 Processor Reference	Manual for Software	Development and Optimization,	at 26-28 (2004)
Intel's Objections to BIAX's Proposed Changes for '755 Patent, Itanium and Itanium 2			Basis For Objection	their associated execution resources"	This is a substantive change in BIAX's infringement	theory. BIAX seeks to accuse a new or different	structure by changing the identified structure from	the "functional elements" to the "issue ports and	their associated execution resources"
Intel's Objections to BIA		Nature of Change to	Contention	execution resources".	-	covering Itanium's "functional	elements" to covering Itanium's	"issue ports and their associated	execution resources".
	Location of	Change in BIAX Ex. D	(Part 1 of 3)		Pg. 74, block	1, line 1	•		

Change in BIAX Ex. D (Part 2 of 3) Contention Pg. 2, block 2, linsert a contention covering line 1 line 30, Pg. 4, block 1, linsert citation to Itanium Sg. 4, block 2, linsert a contention covering line 1 line 1 Pg. 4, block 2, linsert a contention covering line in assembler Pg. 4, block 2, linsert a contention covering line in assembler Pg. 4, block 2, linsert a contention covering line in assembler Pg. 4, block 2, linsert a contention covering line in assembler Pg. 4, block 2, linsert a contention covering line in assembler Pg. 4, block 2, linsert a contention covering line in assembler Pg. 4, block 3, linsert a contention covering line in assembler Pg. 4, block 3, linsert a contention covering line in assembler Pg. 4, block 3, linsert a contention covering line in assembler Pg. 4, block 3, linsert a contention covering line in assembler Pg. 4, block 3, linsert a contention covering line in assembler Pg. 4, block 3, linsert a contention covering line in assembler Itanium sassembler Pg. 4, block 3, linsert citation to Itanium BlaXYs infinigement theory. Bla or different structure by a covering the Itanium asset Itanium compiler. This citation supports the assembler code BlaXYs infinigement theory back covering the Itanium asset Itanium compiler. Pg. 4, block 3, linsert citation to Itanium BlaXYs infinigement theory Bla Itanium's assembler code BlaXYs infinigement theory Bla Itanium's assembler code BlaXYs infinigement theory Bla Itanium's assembler code	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Nature of Change to Contention Insert a contention covering Itanium's assembler assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert a contention covering Itanium's assembler Insert a contention to Itanium assembler code Insert a contention to Itanium assembler code Insert citation to Itanium assembler code		
Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code Itanium's assembler Insert citation to Itanium assembler code Insert contention covering Itanium's assembler Insert a contention covering Itanium's assembler Insert a contention covering Itanium's assembler assembler code Insert citation to Itanium assembler code	Change to	Cite to Public Document
Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code Itanium's assembler Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert a contention covering Itanium's assembler Insert a contention to Itanium assembler code Insert citation to Itanium assembler code		(where applicable)
Itanium's assembler Insert citation to Itanium assembler code Itanium's assembler Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert a contention to Itanium assembler code Insert a contention to Itanium assembler code Insert citation to Itanium assembler code		Ward Decl. Ex. N, Intel®
Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler assembler code Insert citation to Itanium assembler code Itanium's assembler Itanium's assembler assembler code Insert citation to Itanium assembler code	ssembler infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler assembler code Insert a contention covering Itanium's assembler Insert a contention covering Itanium's assembler assembler code	or different structure by adding a contention	Assembly Language Reference
Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler assembler code Insert citation to Itanium assembler code Itanium's assembler Itanium's assembler assembler code	covering the Itanium assembler in addition to the Itanium compiler.	Guide, at 31-33 (2001)
assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	ion to Itanium This citation supports the substantive change to	N/A
Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	code BIAX's infringement theory objected to in the row	
Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	above.	
Itanium's assembler Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code		Ward Decl. Ex. N, Intel®
Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	ussembler infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	or different structure by adding a contention	Assembly Language Reference
Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
Insert citation to Itanium assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	Itanium compiler.	
assembler code Insert a contention covering Itanium's assembler Insert citation to Itanium assembler code	ion to Itanium This citation supports the substantive change to	N/A
block 3, Insert a contention covering Itanium's assembler , block 3, Insert citation to Itanium 11-14 assembler code	code BIAX's infringement theory objected to in the row	
block 3, Insert a contention covering Itanium's assembler , block 3, Insert citation to Itanium 11-14 assembler code	above.	
Itanium's assembler block 3, Insert citation to Itanium assembler code		Ward Decl. Ex. N, Intel®
Insert citation to Itanium assembler code	assembler infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
Insert citation to Itanium assembler code	or different structure by adding a contention	Assembly Language Reference
Insert citation to Itanium assembler code	covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
Insert citation to Itanium assembler code	Itanium compiler.	The state of the s
assembler code		N/A
above.	above.	

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 2 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
Pg. 4, block 4, line 1		This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 6, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 6, block 2, line 1	Insert an additional contention that instruction firing times correspond to "stop bits".	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention that instruction firing times correspond to "stop bits".	Ward Decl. Ex. P, Marsha Eng, et. al, "Mesocode: Optimizations for Improving Fetch Bandwidth of Itanium Processors", Workshop on Complexity-Effective Design (WCED), May 2002, at 2.
Pg 6. block 2, lines 5-6	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A
Pg. 7, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of			
BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
Pg. 7, block 2,	Changes "execution unit type" to	This is a substantive change to BIAX's	Ward Decl. Ex. M, Intel®
line 3	"issue port"	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	*	or different structure by changing the accused	Manual for Software
		structure from the Itanium "execution unit type" to	Development and Optimization,
		the Itanium "issue port".	at 26-28 (2004)
Pg. 7, block 2,	Insert citation to Itanium	This citation supports the substantive change to	N/A
lines 5-6	assembler code	BIAX's infringement theory objected to in the two	
		rows above.	
Pg. 7, block 3,	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 7, block 3,	Insert citation to Itanium	This citation supports the substantive change to	N/A
lines 4-5	assembler code	BIAX's infringement theory objected to in the two	
		rows above.	
Pg. 7. block 5,	Change the contention from	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
line 1	covering Itanium's "functional	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	unit" to covering Itanium's	or different structure by changing the identified	Manual for Software
	"issue ports and their associated	structure from the "functional unit" to the "issue	Development and Optimization,
	execution resources".	ports and their associated execution resources"	at 26-28 (2004)
Pg. 8, block 1,	i	This citation supports the substantive change to	N/A
lines 1-13		BIAX's infringement theory objected to in the row	
		above.	
Pg. 10, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in			
BIAX Ex. D	Nature of Change to	Dogic Low Objection	Cite to Public Document (where annicable)
(Fart 2 01 3)	Contenuon	Dasis For Objection	T. TW 4. Lization
1, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Hanum La Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 10, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, line 15	assembler code	BIAX's infringement theory objected to in the two	
		rows above.	
Pg. 11, block	Change the contention from	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
1, lines 4-5	covering Itanium's "functional	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	unit" to covering Itanium's	or different structure by changing the identified	Manual for Software
	"issue port and its associated	structure from the "functional unit" to the "issue	Development and Optimization,
	execution resources".	port and its associated execution resources"	at 26-28 (2004)
Pg. 11, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
1, lines 6-18	and McKinley code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 12, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
2. line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
`		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 14, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, line 13-15	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 14, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture

Cite to Public Docume BlAX Ex.D Contention Or different structure by adding a contention Deg. 14, block Insert citation to Itanium Bg. 15, block Insert a contention covering Insert a contention coverin		Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
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Insert an additional contention Infinite instruction firing times or different structure by adding a contention that instruction firing times correspond to "stop bits".	3, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
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block Insert an additional contention This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new correspond to "stop bits".			covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
block Insert an additional contention that instruction firing times instruction firing times correspond to "stop bits". This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention that instruction firing times correspond to "stop bits".			Itanium compiler.	
that instruction firing times infringement theory. BIAX seeks to accuse a new correspond to "stop bits".	Pg. 16, block	Insert an additional contention	This is a substantive change to BIAX's	Ward Decl. Ex. P, Marsha Eng,
correspond to "stop bits". or different structure by adding a contention that instruction firing times correspond to "stop bits".	3, line 1	that instruction firing times	infringement theory. BIAX seeks to accuse a new	et. al, "Mesocode: Optimizations
	.	correspond to "stop bits".	or different structure by adding a contention that	for Improving Fetch Bandwidth
		Polymon Polymon	instruction firing times correspond to "stop bits".	of Itanium Frocessors",

Inter's Objections to BIAX's Proposed Changes for '945 Fatent, Itanium and Itanium 2
Basis For Objection
····
Insert citation to Itanium This citation supports the substantive change to
BIAX's infringement theory objected to in the two
rows above.
Insert a contention covering This is a substantive change to BIAX's
infringement theory. BIAX seeks to accuse a new
or different structure by adding a contention
covering the Itanium assembler in addition to the Itanium compiler.
Insert citation to Itanium This citation supports the substantive change to
BIAX's infringement theory objected to in the row
above.
Insert a contention covering This is a substantive change to BIAX's
or different structure by adding a contention
covering the Itanium assembler in addition to the
Itanium compiler.
Insert citation to Itanium This citation supports the substantive change to
BIAX's infringement theory objected to in the row
above.
Insert a contention covering This is a substantive change to BIAX's
infringement theory. BIAX seeks to accuse a new
or different structure by adding a contention

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of			
BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 18, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
1, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 18, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, line 15	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 21, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
`		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 21, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 18-19	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 21, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
3, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 22, block	Changes "execution unit types"	This is a substantive change to BIAX's	Ward Decl. Ex. M, Intel®
1, line 1	to "issue ports"	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference

Location of			
Change in			
	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
		or different structure by changing the accused	Manual for Software
		structure from the Itanium "execution unit types" to	Development and Optimization,
		the Itanium "issue ports".	at 26-28 (2004)
	Change "architectural stops" to	This is a substantive change to BIAX's	Ward Decl. Ex. P, Marsha Eng,
1, line 1	"stop bits"	infringement theory. BIAX seeks to accuse a new	et. al, "Mesocode: Optimizations
	•	or different structure by changing the identified	for Improving Fetch Bandwidth
		structure from "architectural stops" to "stop bits".	of Itanium Processors",
			Workshop on Complexity-
			Effective Design (WCED), May
			2002, at 2.
Pg. 22, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, lines 4-5	assembler code	BIAX's infringement theory objected to in the two	
		rows above.	
Pg. 23, block	Change "functional units" to	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
2, lines 1-2	"execution resources associated	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	with the issue ports".	or different structure by changing the identified	Manual for Software
•	•	structure from the "functional units" to "execution	Development and Optimization,
		resources associated with the issue ports"	at 26-28 (2004)
Pg. 23, block	Change "functional units" to	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
2. line 3	"issue ports".	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	4	or different structure by changing the identified	Manual for Software
		structure from the "functional units" to "issue	Development and Optimization,
		ports"	at 26-28 (2004)
Pg. 23, block	Change "processor" to "port".	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
2, line 3	•	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference

Location of	Intel's Objections to BIAX	AX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2	,
Change in BIAX Ex. D (Part 2 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)	
		or different structure by changing the identified structure from the "processor" to the "port"	Manual for Software Development and Optimization, at 26-28 (2004)	
Pg. 24, block 1, lines 1-3	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the three rows above.	N/A	
Pg. 26, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)	
Pg. 26, block 2, line 11-13	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A	
Pg. 26, block 3, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)	
Pg. 26, block 3, line 10; pg. 27, block 1, lines 1-3	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A	
Pg. 27, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new	Ward Decl. Ex. N, Intel® Itanium TM Architecture	

	Intel's Objections to BIAN	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
		or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Assembly Language Reference Guide, at 31-33 (2001)
Pg. 28, block 2, lines 2-5	Add contention that instruction firing times are determined "by the Itanium compiler".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention that instruction firing times are added "by the Itanium compiler".	N/A
Pg. 28, block 3, line 1	Insert an additional contention that instruction firing times correspond to "stop bits".	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention that instruction firing times correspond to "stop bits".	Ward Decl. Ex. P, Marsha Eng, et. al, "Mesocode: Optimizations for Improving Fetch Bandwidth of Itanium Processors", Workshop on Complexity-Effective Design (WCED), May 2002, at 2.
Pg. 28, block 3, line 2	Add contention that instruction firing times are added "by the Itanium compiler or assembler".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention that instruction firing times are added "by the Itanium compiler or assembler".	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 28, block 3, lines 3-4	Insert citation to Itanium compiler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 28, block 3, lines 4-5	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the	N/A

	Intel's Objections to BIAN	AX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of			
BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
		second row above.	
Pg. 30, block	Insert citation to Itanium	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
1, lines 11-12	assembler code	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 31, block	Insert an additional contention	This is a substantive change to BIAX's	Ward Decl. Ex. P, Marsha Eng,
3. line 1	that instruction firing times	infringement theory. BIAX seeks to accuse a new	et. al, "Mesocode: Optimizations
	correspond to "stop bits".	or different structure by adding a contention that	for Improving Fetch Bandwidth
	4	instruction firing times correspond to "stop bits".	of Itanium Processors",
			Workshop on Complexity-
			Effective Design (WCED), May
			2002, at 2.
Pg. 31. block	Insert citation to Itanium	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
3. lines 4-5	assembler code	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 33. block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
2. line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	<i>Guide</i> , at 31-33 (2001)
		Itanium compiler.	
Pg. 33, block	Changes "execution unit types"	This is a substantive change to BIAX's	Ward Decl. Ex. M, Intel®
2, line 3	to "issue ports"	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
		or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports".	Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 33, block 2, lines 5-6	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A
Pg. 33, block 3, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 33, block 3, lines 4-5	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 34, block 2, line 1	Change "11 issue ports" to "the ISD (instruction dispersal) unit"	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure for storing instructions from the "11 issue ports" to "the ISD (instruction dispersal) unit"	Ward Decl. Ex. Q, Intel® Itanium® 2 Processor Hardware Developer's Manual, at 2-4 to 2- 5 (2002)
Pg. 34, block 2, lines 4-20	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 35, block 2, line 1	Change "functional units" to "issue ports".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software

I ocation of			
Change in			
	Nature of Change to		Cite to Public Document
(Part 2 of 3) (Contention	Basis For Objection	(where applicable)
		structure from the "functional units" to the "issue	Development and Optimization,
		ports".	at 20-20 (2004)
Pg. 35, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
	and McKinley code	BIAX's intringement theory objected to in the row above.	
Pg. 35. block (Change the contention from	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
-	covering Itanium's "functional	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	unit" to covering Itanium's	or different structure by changing the identified	Manual for Software
,	"issue port and its associated	structure from the "functional unit" to the "issue	Development and Optimization,
)	execution resources".	port and its associated execution resources"	at 26-28 (2004)
1	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
3, lines 4-6;	and McKinley code	BIAX's infringement theory objected to in the row	
pg. 36, block		above.	
+	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 41, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 41, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
3, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium ^{1M} Architecture
		or different structure by adding a contention	Assembly Language reference

Change in BIAX Ex. D Contention Cover (Part 2 of 3) Contention Cover (Part 2 of 3) Contention Cover (Itania Pg. 42, block Change "architectural stops" to "infrint This 1, line 2 Pg. 42, block This assembler code Pg. 43, block I, line 2.6 assembler code This 1, line 2.8 Pg. 43, block This 2.6 Assembler code Tows Pg. 43, block This 2.6 Assembler code This 1, line 2.8 Pg. 43, block This 2.6 Assembler code This 1, line 2.8 Pg. 43, block This 2.6 This 3.6 This 3.6 This 3.6 This 3.6 This 4.6 This 4		
Nature of Change to Contention Changes "execution unit types" to "issue ports" in "stop bits" Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated "execution resources associated		
Changes "execution unit types" to "issue ports" to "issue ports" Change "architectural stops" to "stop bits' Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated "execution resources associated		Cite to Public Document
Changes "execution unit types" to "issue ports" in the change "architectural stops" to "stop bits" in the code insert citation to Itanium assembler code insert citation to Itanium assembler code Change "functional units" to "execution resources associated "execution resources associated in the code control of the code in the cod	Basis For Objection	(where applicable)
Changes "execution unit types" to "issue ports" in the Change "architectural stops" to "stop bits" in the Insert citation to Itanium assembler code in the Insert citation to Itanium assembler code in the Change "functional units" to "execution resources associated "execution resources associated to "execution resources associated".	covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
change "architectural stops" to "stop bits' Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated	This is a substantive change to BIAX's	Ward Decl. Ex. M, Intel®
Change "architectural stops" to "stop bits" Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
Change "architectural stops" to "stop bits" Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated"		Manual for Software
Change "architectural stops" to "stop bits" Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated"	es" to	Development and Optimization,
Change "architectural stops" to "stop bits" Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated	the Itanium "issue ports".	at 26-28 (2004)
"stop bits' Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		Ward Decl. Ex. P, Marsha Eng,
Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated"	infringement theory. BIAX seeks to accuse a new	et. al, "Mesocode: Optimizations
Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		for Improving Fetch Bandwidth
Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		of Itanium Processors",
Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		Workshop on Complexity-
Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		Effective Design (WCED), May
Insert citation to Itanium assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		2002, at 2.
assembler code Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		N/A
Insert citation to Itanium assembler code Change "functional units" to "execution resources associated	BIAX's infringement theory objected to in the three	
Insert citation to Itanium assembler code Change "functional units" to "execution resources associated		7 / 1 / 4
assembler code Change "functional units" to "execution resources associated		N/A
Change "functional units" to "execution resources associated	BIAX's infringement theory objected to in the three	
Change "functional units" to "execution resources associated	rows above.	A CARACTER STATE OF THE STATE O
"execution resources associated		Ward Decl. Ex. M, Intel®
	ted	Itanium® 2 Processor Reference
with the issue ports".	or different structure by changing the identified	Manual for Software
	structure from the "functional units" to "execution	Development and Optimization,
resol	resources associated with the issue ports"	at 26-28 (2004)

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
Pg. 44, block 1, line 1	Change "functional units" to "issue ports".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to "issue ports".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 44, block 1, lines 4-20	Insert citation to Itanium Merced and McKinley code	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	N/A
Pg. 47, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 47, block 2, lines 11-13	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 48, block 1, line 2	Insert citation to Itanium assembler code	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 48, block 2, line 1	Insert a contention covering Itanium's assembler	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the	Ward Decl. Ex. N, Intel® Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of			
Change in			Cite to Public Document
BIAX Ex. D	Nature of Change to Contention	Basis For Objection	(where applicable)
(1 4117 013)		Itanium compiler.	
Po 49 block	Add contention that instruction	This is a substantive change in BIAX's	Ward Decl. Ex. N, Intel®
2 line 2	firing times are determined "by	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
1	the Itanium compiler or	or different structure by adding a contention that	Assembly Language Reference
	assembler".	instruction firing times are determined "by the Itanium compiler or assembler".	Guide, at 31-33 (2001)
Po 49 block	Insert an additional contention	This is a substantive change to BIAX's	Ward Decl. Ex. P, Marsha Eng,
2 line 1	that instruction firing times	infringement theory. BIAX seeks to accuse a new	et. al, "Mesocode: Optimizations
), mile i	correspond to "stop bits".	or different structure by adding a contention that	for Improving Fetch Bandwidth
	4	instruction firing times correspond to "stop bits".	of Itanium Processors",
		1	Workshop on Complexity-
			Effective Design (WCED), May
			2002, at 2.
Pg. 52, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 4-5	assembler code	BIAX's infringement theory objected to in the two	
Do 69 blook	Insert an additional contention	This is a substantive change to BIAX's	Ward Decl. Ex. P, Marsha Eng,
Fg. 32, DIOCA 3 line 1	that instruction firing times	infringement theory. BIAX seeks to accuse a new	et. al, "Mesocode: Optimizations
7, IIIIV 1	correspond to "stop hits".	or different structure by adding a contention that	for Improving Fetch Bandwidth
	· sur dons on purodeoritos	instruction firing times correspond to "stop bits".	of Itanium Processors",
			Workshop on Complexity-
			Effective Design (WCED), May
			2002, at 2.
10014 CZ NO	Add contention that instruction	This is a substantive change in BIAX's	Ward Decl. Ex. N, Intel®
1 g, 32, 010ch 3, line 2	firing times are added to each	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture

Change in BIAX Ex. D Nature of Change to (Part 2 of 3) Contention instruction "by the Itanium compiler or assembler". Pg. 53, block Insert citation to Itanium 1, lines 29-30 assembler code Pg. 55, block Insert a contention covering 2, line 1 Itanium's assembler	ange to y the Itanium sembler".		
of 3) of 3) olock 29-30 olock	nium 		
of 3) slock slock			Cite to Public Document (where annlicable)
slock 29-30 slock		Basis For Objection	(Augusta appropriate of the control
olock 29-30 olock		or different structure by adding a contention that	Assembly Language Reference
olock 29-30 olock		instruction firing times are added to each instruction "by the Itanium compiler or assembler".	Guide, at 51-55 (2001)
29-30 slock	to Itanium	This citation supports the substantive change to	N/A
olock	e_	BIAX's infringement theory objected to in the two	
olock		rows above.	
	ntion covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
	embler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	<i>Guide</i> , at 31-33 (2001)
		Itanium compiler.	
-	Changes "execution unit type" to	This is a substantive change to BIAX's	Ward Decl. Ex. M, Intel®
2 line 3 "issue nort"		infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
		or different structure by changing the accused	Manual for Software
		structure from the Itanium "execution unit type" to	Development and Optimization,
		the Itanium "issue port".	at 26-28 (2004)
Pg. 55, block Insert citation to Itanium	to Itanium	This citation supports the substantive change to	N/A
2, lines 11-12 assembler code	de	BIAX's infringement theory objected to in the two	
		rows above.	
Po 55 block Insert a conter	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
	embler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 55, block Insert citation to Itanium	a to Itanium	This citation supports the substantive change to	N/A

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in			
BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
3, lines 4-5	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 56, block	Change "11 issue ports" to "the	This is a substantive change in BIAX's	Ward Decl. Ex. Q, Intel®
2, lines 1-2	ISD (instruction dispersal) unit"	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Hardware
\		or different structure by changing the identified	Developer's Manual, at 2-4 to 2-
		structure for storing instructions from the "11 issue	5 (2002)
		ports" to "the ISD (instruction dispersal) unit"	The state of the s
Pg. 56, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
2, lines 4-20	and McKinley code	BIAX's infringement theory objected to in the row	
		above.	TO THE TAXABLE STATE OF
Pg. 57, block	Change "functional units" to	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
2, line 1	"ssue ports".	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
`	•	or different structure by changing the identified	Manual for Software
		structure from the "functional units" to the "issue	Development and Optimization,
		ports".	at 26-28 (2004)
Pg. 57, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
2, lines 3-6	and McKinley code	BIAX's infringement theory objected to in the row	
-		above.	
Pg. 57, block	Change "functional unit" to	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
3. line 1	"issue port".	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	4	or different structure by changing the identified	Manual for Software
		structure from the "functional unit" to the "issue	Development and Optimization,
		port".	at 26-28 (2004)
Pg. 57, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
3, lines 4-0;	and McMilley code	DIANS IIIIIII BOIIINIII TIIOI) OOLOOGA WAT TIIO IOA	

anium and Itanium 2	Cite to Public Document (where applicable)		Ward Decl. Ex. N, Intel® 1se a new Itanium TM Architecture ion Assembly Language Reference on to the Guide, at 31-33 (2001)	· · · · · · · · · · · · · · · · · · ·	ange to N/A in the two		ange to N/A in the row
AX's Proposed Changes for '945 Patent. Itanium and Itanium 2	Basis For Objection	above.	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention that instruction firing times correspond to "stop bits".	This citation supports the substantive change to BIAX's infringement theory objected to in the two rows above.	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure for storing instructions from the "11 issue ports" to "the ISD (instruction dispersal) unit"	This citation supports the substantive change to BIAX's infringement theory objected to in the row
Intel's Objections to BIAX	Nature of Change to Contention		Insert a contention covering Itanium's assembler	Insert an additional contention that instruction firing times correspond to "stop bits".	Insert citation to Itanium assembler code	Change "11 issue ports" to "the ISD (instruction dispersal) unit"	Insert citation to Itanium Merced and McKinley code
	Location of Change in BIAX Ex. D (Part 2 of 3)	pg. 58, block 1, line 1	pg. 62, block 2, lines 1-2	Pg. 62, block 2, line 3	Pg. 62, block 2, lines 6-8	Pg. 64, block 2, line 2	Pg. 64, block 2, lines 4-18;

	Intel's Objections to BIAX	AX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in			
BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
1, lines 1-3		The state of the s	
Pg. 65, block	Change the contention from	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
2, lines 1-2	covering Itanium's "functional	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	units" to covering Itanium's	or different structure by changing the identified	Manual for Software
	"issue ports and their associated	structure from the "functional units" to the "issue	Development and Optimization,
	execution resources".	ports and their associated execution resources"	at 26-28 (2004)
Pg. 65, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
2, lines 5-17;	and McKinley code	BIAX's infringement theory objected to in the row	
Pg. 66, block 1. lines 1-3		above.	
pg. 68, block	Change the contention from	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
1, line 1	covering Itanium's "processor	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	elements" to covering Itanium's	or different structure by changing the identified	Manual for Software
	"issue ports and their associated	structure from the "processor elements" to the	Development and Optimization,
	execution resources".	"issue ports and their associated execution resources"	at 26-28 (2004)
Pg. 73, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
2, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
		or different structure by adding a contention	Assembly Language Reference
		covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
		Itanium compiler.	
Pg. 73, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
2, lines 7-9	assembler code	BIAX's infringement theory objected to in the row	
		above.	
Pg. 73, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®

	Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D (Part 2 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
3, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler.	Itanium TM Architecture Assembly Language Reference Guide, at 31-33 (2001)
Pg. 73, block 3, line 5	Changes "execution unit type" to "issue port"	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit type" to the Itanium "issue port".	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 73, block 3, line 5	Change "architectural stops" to "stop bits'	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	Ward Decl. Ex. P, Marsha Eng, et. al, "Mesocode: Optimizations for Improving Fetch Bandwidth of Itanium Processors", Workshop on Complexity-Effective Design (WCED), May 2002, at 2.
Pg. 75, block 1, lines 13-14	Insert citation to Itanium assembler code	This citation supports the substantive change to BIAX's infringement theory objected to in the three rows above.	N/A
Pg. 75, block 2, lines 1-4	Change the contention from covering Itanium's "functional units" to covering Itanium's "issue ports and their associated execution resources".	This is a substantive change in BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from the "functional units" to the "issue ports and their associated execution resources"	Ward Decl. Ex. M, Intel® Itanium® 2 Processor Reference Manual for Software Development and Optimization, at 26-28 (2004)
Pg. 75, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A

Cite to Public Document (Where applicable) 2. limes 6-9 and McKinley code Basis For Objection 2. limes 6-9 and McKinley code Basis For Objection (Where applicable) 2. limes 6-9 and McKinley code Basis For Objection This is a substantive change to BIAX's infingement theory. BIAX seeks to accuse a new formium by Architecture overing the famium assembler in addition to the fastembly Language Reference overing the famium assembler in addition to the finantium's assembler code Be 79, block Insert a contention covering This is a substantive change to BIAX's infingement theory. BIAX seeks to accuse a new funnium by Architecture assembler code By 79, block Insert a contention covering the famium assembler in addition to the finantium's assembler code By 79, block Changes "execution unit types" infingement theory. BIAX seeks to accuse a new funnium by Architecture or different structure by adding a contention covering the famium assembler in addition to the funnium by Architecture assembler in addition to the covering the famium searcher in addition to the funnium by adding a contention or different structure by adding a contention of cuite, at 31-33 (2001) By 79, block Changes "execution unit types" This is a substantive change to BIAX's infingement theory. BIAX seeks to accuse a new funnium by a contention or different structure from the Itanium "execution unit types" to a "structure from the Itanium "structure from the Itanium "structure form the Itanium searcher is a substantive change to BIAX's accident and Optimizations or different structure from "architectural stops" to a "structure from "architectural stops" to "different structure from "architect		Intel's Objections to BIAX	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Contention and McKinley code and McKinley code BIAX's infringement theory objected to in the row above. Insert a contention covering Infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Insert citation to Itanium BIAX's infringement theory objected to in the row above. Insert a contention covering Insi is a substantive change to BIAX's infringement theory objected to in the row above. Insert a contention covering Infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" Change "architectural stops" to "lissue ports" Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	Location of			
and McKinley code BIAX's infringement theory objected to in the row above. Insert a contention covering Itanium's assembler Insert citation to Itanium Insert a contention covering Itanium compiler. Insert a contention covering Itanium compiler. Insert a contention covering Itanium compiler. Insert a contention covering Itanium sasembler in addition to the Itanium's assembler code BIAX's infringement theory objected to in the row above. Insert a contention covering Itanium substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium compiler. Change "architectural stops" to this is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". In this is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	BIAX Ex. D	Nature of Change to		Cite to Public Document
and McKinley code above. Insert a contention covering This is a substantive change to BIAX's Infinigement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium compiler. Insert citation to Itanium This citation supports the substantive change to assembler code BIAX's infinigement theory objected to in the row above. Insert a contention covering This is a substantive change to BIAX's infinigement theory objected to in the row above. Insert a contention covering This is a substantive change to BIAX's infinigement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" This is a substantive change to BIAX's infinigement theory. BIAX seeks to accuse a new or different structure by changing the accused a structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infinigement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
Insert a contention covering infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Insert citation to Itanium Covering the Itanium assembler in addition to the Itanium compiler. Insert a contention covering Insert a contention to the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" In infinitement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". This is a substantive change to BIAX's infinigement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	2, lines 6-9	and McKinley code	BIAX's infringement theory objected to in the row above.	
Itanium's assembler or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Insert citation to Itanium compiler. Insert a contention covering BIAX's infringement theory objected to in the row above. Insert a contention covering This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure from the Itanium "issue ports". This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits."	Pg. 79, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. This citation supports the substantive change to BIAX's infringement theory objected to in the row above. Insert a contention covering Itanium's assembler Changes "execution unit types" to "issue ports" Change "architectural stops" to the Itanium "issue ports". This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	2, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
Insert citation to Itanium compiler. Insert citation to Itanium This citation supports the substantive change to assembler code BIAX's infringement theory objected to in the row above. Insert a contention covering Itanium's assembler Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".			or different structure by adding a contention	Assembly Language Reference
Insert citation to Itanium SIAX's infringement theory objected to in the row above. Insert a contention covering Ifanium's assembler Changes "execution unit types" Change "architectural stops" to "fix is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium compiler. Changes "execution unit types" Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure from the Itanium "issue ports". This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".			covering the Itanium assembler in addition to the Itanium compiler.	Guide, at 31-33 (2001)
Insert a contention covering Insert a contention covering Itanium's assembler Changes "execution unit types" Change "architectural stops" to "stop bits" Change "architectural stops" to "the Itanium "issue ports". This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "the Itanium" types" to "the Itanium" "issue ports".	Pg. 79. block	Insert citation to Itanium	This citation supports the substantive change to	N/A
Insert a contention covering Itanium's assembler Itanium's assembler Changes "execution unit types" Infinigement theory. BIAX seeks to accuse a new or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	2, lines 8-9	assembler code	BIAX's infringement theory objected to in the row	
Insert a contention covering Itanium's assembler Changes "execution unit types" to "issue ports" Change "architectural stops" to "stop bits" Change "architectural stops" Change "architectural stops" to "infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to different structure by changing the identified structure from "architectural stops" to "stop bits".			above.	
Itanium's assembler or different structure by adding a contention covering the Itanium assembler in addition to the Itanium compiler. Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	Pg. 79, block	Insert a contention covering	This is a substantive change to BIAX's	Ward Decl. Ex. N, Intel®
Changes "execution unit types" Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "issue ports". Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	3, line 1	Itanium's assembler	infringement theory. BIAX seeks to accuse a new	Itanium TM Architecture
Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".			or different structure by adding a contention	Assembly Language Reference
Changes "execution unit types" This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".			covering the Itanium assembler in addition to the	Guide, at 31-33 (2001)
Changes "execution unit types" to "issue ports" or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure from "architectural stops" to "stop bits".			Itanium compiler.	
to "issue ports" infringement theory. BIAX seeks to accuse a new or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	Pg. 79, block	Changes "execution unit types"	This is a substantive change to BIAX's	Ward Decl. Ex. M, Intel®
or different structure by changing the accused structure from the Itanium "execution unit types" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	3, line 5	to "issue ports"	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
Change "architectural stops" to the Itanium "issue ports". Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".			or different structure by changing the accused	Manual for Software
Change "architectural stops" to This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".			structure from the Itanium "execution unit types" to	Development and Optimization,
Change "architectural stops" to "stop bits" infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".			the Itanium "issue ports".	at 26-28 (2004)
"stop bits' infringement theory. BIAX seeks to accuse a new or different structure by changing the identified structure from "architectural stops" to "stop bits".	Pg. 79, block	Change "architectural stops" to	This is a substantive change to BIAX's	Ward Decl. Ex. P, Marsha Eng,
or different structure by changing the identified structure from "architectural stops" to "stop bits".	3, line 5	"stop bits"	infringement theory. BIAX seeks to accuse a new	et. al, "Mesocode: Optimizations
		-	or different structure by changing the identified	for Improving Fetch Bandwidth
			structure from "architectural stops" to "stop bits".	of Itanium Processors",
			The state of the s	Workshop on Complexity-

	Intel's Objections to BIAN	Intel's Objections to BIAX's Proposed Changes for '945 Patent, Itanium and Itanium 2	Itanium 2
Location of Change in BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 2 of 3)	Contention	Basis For Objection	(where applicable)
			Effective Design (WCED), May 2002, at 2.
Pg. 81, block	Insert citation to Itanium	This citation supports the substantive change to	N/A
1, line 13-15	assembler code	BIAX's infringement theory objected to in the three rows above.	
Pg. 81, block	Change the contention from	This is a substantive change in BIAX's	Ward Decl. Ex. M, Intel®
2, lines 1-3	covering Itanium's "functional	infringement theory. BIAX seeks to accuse a new	Itanium® 2 Processor Reference
	units" to covering Itanium's	or different structure by changing the identified	Manual for Software
	"issue ports".	structure from the "functional units" to the "issue ports"	Development and Optimization, at 26-28 (2004)
Pg. 81, block	Insert citation to Itanium Merced	This citation supports the substantive change to	N/A
2, lines 5-15;	and McKinley code	BIAX's infringement theory objected to in the row	
pg. 82, block		above.	
1, lines 1-6	The state of the s		

Intel's Objections to BIAX's Proposed Changes for '755 Patent, Hyper-Threading Products

ф р д <u>я</u> 9		Nature of Change to		Cite to Public Document
Added three paragraphs of new contentions relating to the use of thread-ids in the accused products, the presence of two instruction queues, and how the threads are allocated to the instruction queues Insert citation to Prescott code having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries		tention	Basis For Objection	(where applicable)
contentions relating to the use of thread-ids in the accused products, the presence of two instruction queues, and how the threads are allocated to the instruction queues Insert citation to Prescott code to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries	~;	ed three paragraphs of new	This is a substantive change to BIAX's	Ward Decl. Ex. R, Deborah T.
thread-ids in the accused products, the presence of two instruction queues, and how the threads are allocated to the instruction queues Insert citation to Prescott code Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries		entions relating to the use of	infringement theory. BIAX seeks to accuse a new	Marr, et al, Hyper-Threading
products, the presence of two instruction queues, and how the threads are allocated to the instruction queues Insert citation to Prescott code Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries		ad-ids in the accused	or different structure by adding a new contention	Technology Architecture and
instruction queues, and how the threads are allocated to the instruction queues Insert citation to Prescott code Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries		lucts, the presence of two	covering the thread-ids and instruction queues used	Microarchitecture, at 9-10
threads are allocated to the instruction queues Insert citation to Prescott code Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries	instr	ruction queues, and how the	in the accused products.	(2002)
instruction queues Insert citation to Prescott code Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to	thre	ads are allocated to the		
Insert citation to Prescott code Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to	instr	ruction queues		
Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries		rt citation to Prescott code	This citation supports the substantive change to	N/A
Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to increase via the TC entries	nes 12, 14-		BIAX's infringement theory objected to in the row	
Removal of contention relating to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to increase with the TC entries	7, 21		above.	
to logical resource drivers having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to increasing the TC entries		noval of contention relating	This is a substantive change to BIAX's	Ward Decl. Ex. R, Deborah T.
having register rename logic and Register Alias Tables. Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to increase via the TC entries		gical resource drivers	infringement theory. BIAX seeks to change	Marr, et al, Hyper-Threading
Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries	havi	ing register rename logic and	positions by removing this contention relating to	Technology Architecture and
Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries	Reg	ister Alias Tables.	logical resource drivers having register rename	Microarchitecture, at 10 (2002)
Addition of contention that each thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries			logic and Register Alias Tables.	
thread has its own Register Alias Table Removal of contention relating to how the logical resource drivers add thread information to		lition of contention that each	This is a substantive change to BIAX's	Ward Decl. Ex. R, Deborah T.
Table Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries		ad has its own Register Alias	infringement theory. BIAX seeks to accuse a new	Marr, et al, Hyper-Threading
Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries	Tab	le	or different structure by adding contention that each	Technology Architecture and
Removal of contention relating to how the logical resource drivers add thread information to instructions via the TC entries			thread has its own Register Alias Table.	Microarchitecture, at 10 (2002)
to how the logical resource drivers add thread information to		noval of contention relating	This is a substantive change to BIAX's	Ward Decl. Ex. R, Deborah T.
		ow the logical resource	infringement theory. BIAX seeks to seeks to	Marr, et al, Hyper-Threading
	driv	ers add thread information to	change positions by removing this contention	Technology Architecture and
	inst	instructions, via the TC entries.	relating to how the logical resource drivers add	Microarchitecture, at 9 (2002)

Location of Change in BIAX Ex. D (Part 3 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where annlicable)
		thread information to instructions, via the TC entries.	
Pg. 3, block 2, lines 14-16	Insert citation to Prescott code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 3, block 2, lines 18-20	Removal of contention relating to how the DTLB is used to translate addresses to physical addresses.	This is a substantive change to BIAX's infringement theory. BIAX seeks to seeks to change positions by removing this contention relating to how the DTLB is used to translate addresses to physical addresses.	Ward Decl. Ex. R, Deborah T. Marr, et al, Hyper-Threading Technology Architecture and Microarchitecture, at 11 (2002)
Pg. 3, block 3, line 1; pg. 4, block 1, line 1	Changing contention that Intel Hyper-Threading processors have "a plurality of execution units" to contention that the processors have "three ports, each port having execution units".	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the contention to add the "three ports" (i.e. issue ports).	Ward Decl. Ex. L, The Microarchitecture of the Pentium 4 Processor, Intel Technology Journal Vol. 5, Issue 1, February, 2001, at 7-8
Pg. 4, block 4, lines 1-4	Insert citation to Prescott code	This citation supports the substantive change to BIAX's infringement theory objected to in the row above.	N/A
Pg. 4, block 1, lines 16-19	Remove contention that Figure 6 "shows three execution units receiving simple instructions from two logical resource drivers. Each logical resource driver includes its own register rename block. The execution	This is a substantive change to BIAX's infringement theory. BIAX seeks to seeks to change positions by removing this contention relating to how the execution units receive instructions from the logical resource drivers.	Ward Decl. Ex. R, Deborah T. Marr, et al, <i>Hyper-Threading</i> <i>Technology Architecture and</i> <i>Microarchitecture</i> , at 10 (2002)

Location of Change in BIAX Ex. D (Part 3 of 3)	Nature of Change to Contention	Basis For Objection	Cite to Public Document (where applicable)
	units receive simple instructions and complex instructions decoded into uops in a determined order."		
Pg. 4, block 1, lines 21-22	Addition of contention that Figure 6 "shows three execution units receiving instructions from the two queues. The ports and their execution units receive instructions in a determined order."	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by adding a different contention relating to how the ports and execution units receive instructions from the queues.	Ward Decl. Ex. R, Deborah T. Marr, et al, Hyper-Threading Technology Architecture and Microarchitecture, at 10 (2002)
Pg. 5, block 1, line 2	Changes "execution unit" to "port"	This is a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the structure identified as corresponding to the claim element.	Ward Decl. Ex. L, The Microarchitecture of the Pentium 4 Processor, Intel Technology Journal Vol. 5, Issue 1, February, 2001, at 7-8
Pg. 5, block 1, line 7	Inserts "ports and their" before "execution units"	This a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the structure identified as corresponding to the claim element, to add additional structure.	Ward Decl. Ex. L, The Microarchitecture of the Pentium 4 Processor, Intel Technology Journal Vol. 5, Issue 1, February, 2001, at 7-8
Pg. 5, block 1, lines 8-10	Replaces "user context information added to the simple instructions and complex instructions decoded into uops" with "thread-ids added to the instructions"	This a substantive change to BIAX's infringement theory. BIAX seeks to accuse a new or different structure by changing the structure (user context information, simple instructions, complex instructions decoded into uops) identified as corresponding to the claim element, to add different	Ward Decl. Ex. R, Deborah T. Marr, et al, Hyper-Threading Technology Architecture and Microarchitecture, at 9 (2002)

Location of Change in BIAX Ex. D	Nature of Change to		Cite to Public Document
(Part 3 of 3)	Contention	Basis For Objection	(where applicable)
		structure (thread-ids, instructions).	
Pg. 5, block 1,	Pg. 5, block 1, Insert citation to Prescott code	This citation supports the substantive change to	N/A
lines 10-12		BIAX's infingement theory objected to in the row	
		above.	
Pg. 5, block 1,	Pg. 5, block 1, Removal of contention	This is a substantive change to BIAX's	Ward Decl. Ex. L, The
lines 14-15;	describing the execution units	infringement theory. BIAX seeks to seeks to	Microarchitecture of the
pg. 6, block 1,	and how they receive data to	change positions by removing this contention	Pentium 4 Processor, Intel
lines 1-2	work on.	relating to how the execution units receive their	Technology Journal Vol. 5, Issue
		data.	1, February, 2001, at 7-8